

## Electronic Patent Application Fee Transmittal

<b>Application Number:</b>	10008700			
<b>Filing Date:</b>	07-Dec-2001			
<b>Title of Invention:</b>	METHODS OF MANUFACTURING INTEGRATED CIRCUIT DEVICES HAVING AN ENCAPSULATED INSULATION LAYER			
First Named Inventor/Applicant Name:	Hong-Sik Jeong			
<b>Filer:</b>	David Moore/Candi Riggs			
<b>Attorney Docket Number:</b>	5649-905			
Filed as Large Entity				
<b>Utility      Filing Fees</b>				
<b>Description</b>	<b>Fee Code</b>	<b>Quantity</b>	<b>Amount</b>	<b>Sub-Total in USD(\$)</b>
<b>Basic Filing:</b>				
<b>Pages:</b>				
<b>Claims:</b>				
<b>Miscellaneous-Filing:</b>				
<b>Petition:</b>				
<b>Patent-Appeals-and-Interference:</b>				
Post-Allowance-and-Post-Issuance:				
Utility Appl issue fee	1501	1	1440	1440
Publ. Fee- early, voluntary, or normal	1504	1	300	300

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Extension-of-Time:				
Miscellaneous:				
Total in USD (\$)				1740